IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Group:) Art Unit:
Gary Gilliam) Examiner:
Serial No.:))
Filed: Filed Herewith) POWER OF ATTORNEY BY ASSIGNEE
For: ON-CHIP SUBSTRATE REGULATOR TEST MODE)))
Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231	_
Sir:	
08/16/05	e of the above-identified application by Assignment dated Reg. No. 34,095) and also the members of the firm of
	nomas B. Haverstock (Reg. No. 32,571) and Jonathan O.
Owens (Reg. No. 37,902), 260 Sheridan Avenue, Suite	e 420, Palo Alto, California 94306, (415) 833-0160, as its
attorneys with full power of substitution to prosecute t	this application and to transact all business in the Patent
and Trademark Office in connection therewith.	•
Please direct all correspondence regarding this	s application to the following:
Thomas B. Haverstock HAVERSTOCK & ASSOCIATES 260 Sheridan Avenue, Suite 420 Palo Alto, California 94306 Telephone: (415) 833-0160 Facsimile: (415) 833-0170	;
, ,	t filed with the application or filed subsequent to the filing y certify that, to the best of my knowledge and belief, title
MICRON	TECHNOLOGY, INC.
Dated: August 29, 1995 By: W	Buyan Fany

Title: Vice President of Legal Affairs and General Counsel

<u>S/N 08/520,818</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Gary R. Gilliam

Examiner: Unknown

Serial No.:

08/520,818

Group Art Unit: 2213

Filed:

August 30, 1995

Docket: 303.221US1

Title:

ON-CHIP SUBSTRATE REGULATOR TEST MODE

REVOCATION/POWER OF ATTORNEY CERTIFICATE UNDER 37 CFR 3.73(b)

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

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In accordance with 37 C.F.R. Section 1.32, M.P.E.P. Section 402.05, 402.07, and 324 please revoke any existing Powers of Attorney, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark

Slifer, Russell D. Viksnins, Ann S.

Woessner, Warren D.

Reg. No. 39,838

Reg. No. 37,748

Reg. No. 30,440

Office in connection therewith:

Reg. No. 39,610
Reg. No. 38,080
Reg. No. 32,836
Reg. No. 35,075
Reg. No. 35,836
Reg. No. 38,107
Reg. No. 39,662
Reg. No. 39,665
Reg. No. 32,651
Reg. No. 35,138
Reg. No. 30,837
Reg. No. 33,235
Reg. No. 37,850
Reg. No. 24,207
Reg. No. 32,146
Reg. No. 36,198
Reg. No. 39,746
Reg. No. 30,568
Reg. No. 30,871
Reg. No. 34,095
Reg. No. 25,816

Serial Number: 08/520,818 Filing Date: August 30, 1995

Title: ON-CHIP SUBSTRATE REGULATOR TEST MODE

CERTIFICATE UNDER 37 CFR §3.73(b)

Micron Technology, Inc. hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventors which is recorded on Reel 7632 and Frame 0041, dated August 30, 1995.

I have reviewed all the documents in the chain of title of the patent application identified above and, to the best of my knowledge and belief, title is in the assignee, Micron Technology, Inc.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Michael L. Lynch, am empowered to sign this certificate on behalf of the assignee, Micron Technology, Inc.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. Box 2938
Minneapolis, Minnesota 55402
Telephone 612/339-0331.

Date: Ext 8,1996

Michael L. Lynch

Title: Chief Patent Counsel